



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of: **KIRA, et al.**

Serial No.: **09/548,313**

Filed: **April 12, 2000**

Group Art Unit: **2652**

Examiner: **Craig A. Renner**

PTO Confirmation: **6169**

FOR: **HEAD ASSEMBLY HAVING INTEGRATED CIRCUIT CHIP COVERED BY LAYER WHICH PREVENTS FOREIGN PARTICLE GENERATION (As Amended)**

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
Washington, D.C. 20231

**RECEIVED**

**JUL 30 2002**

**Technology Center 2600**  
Date: July 25, 2002

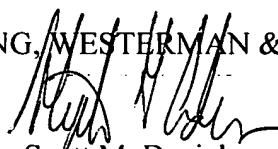
Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated March 28, 2002 for one (1) month from June 28, 2002 to July 29, 2002, July 28, 2002 falling on a Sunday.

Attached please find a check in the amount of \$110.00 to cover the cost of the extension for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 01-2340. Two copies of this paper are enclosed herewith.

Respectfully Submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP

  
Scott M. Daniels  
Attorney for Applicant  
Reg. No. 32,562

07/23/2002 KANMED1 00000053 09548313

01 FC:115 110.00 OP

SMD/rer  
Atty. Docket No. **000452**  
Suite 1000, 1725 K Street, N.W.  
Washington, D.C. 20006  
(202) 659-2930



23850  
PATENT TRADEMARK OFFICE